Time Stamp	2005/11/29 18:49	2005/11/29 18:49	2005/11/29 18:49	2005/11/29 18:49	2005/11/29 18:49	2005/11/29 18:49	2005/11/29 18:50	2005/11/29 18:50	2005/11/29 18:50	2005/11/29 18:50
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Search Query	703/28.ccts.	(US-5937179-\$ or US-5684721-\$ or US-5329471-\$ or US-6691268-\$ or US-5329471-\$ or US-546562-\$ or US-635662-\$ or US-635662-\$ or US-6329300-\$ or US-6188975-\$ or US-620212489-\$ or US-6209120-\$).did. or (US-632912489-\$ or US-6209120-\$).did. or (US-63293189-\$ or US-6399120-\$).did. did.	L2 and (eda and model and bus (reconfig\$ FPGA PLD) and memory)	L3 and (shared adj memory)	L2 and (eda and model and bus and (reconfig\$ FPGA PLD) and memory)	eda and (((shar‡3 common central host) adj3 memory) with host) adj3 memory) with co-simulation co-simulation co-emulation coverification co-verification))	eda and ((shar‡3 common central target) adj3 memory) with co-simulation cosimulation co-emulation co-emulation coverification co-verification))	((shared common host joint target) with memory) and (hardware with software) and (co-verification coverification cosimulation co-simulation)	(bus) and (shared with memory) and (eda (electronic with design with automation))	703/28.ccls.
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Search History 11/29/2005 6:50:49 PM Page 1 C:\Documents and Settings\asaxena\My Documents\EAST\Workspaces\09954715.wsp

13.	12	(hardware with software with (co-verification coverification).ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	క	OFF	2005/06/03 11:22	
ន	Э.	("6321366" "6810442" "6754763").pn.	USPAT	ĕ	PF	2005/06/03 10:45	
ኧ	843	(Swoboda).in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	క	FF0	2005/06/03 10:46	
R	29	(profit).in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	క	PF0	2005/06/03 10:47	
<i>አ</i>	ধ্য	"703".clas. and S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	క	THO .	2005/06/03 10:58	
25	H	"716".clas. and S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	g	TFO OFF	2005/06/03 10:59	
8	16	(FPCA PLD (reconfigurable with logic)) and (DMA) and (testbench)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	క	OFF	2005/06/03 11:04	
8	149	(EDA) and DMA	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	g	OFF	2005/06/03 11:05	
S10	88	(hardware with software with (co-verification coverification))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	S.	FPO FPO	2005/06/03 12:07	

Search History 11/29/2005 6:50:49 PM Page 2 C:\Documents and Settings\asaxena\My Documents\EAST\Workspaces\09954715.wsp

2005/06/03 11:23	2005/06/03 11:40	2005/06/03 11:44	2005/06/03 11:48	2005/06/03 11:49	2005/06/06 15:04	2005/06/03 11:58	2005/06/03 11:58	2005/11/29 16:13	2005/06/06 15:50	2005/06/03 12:00	2005/06/03 12:00	2005/06/03 12:07
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USPAT	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	USPAT	USPAT	USPAT	USPAT	USPAT	USPAT	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB				
("5493723" "6356862" "6223272" "6223144" "5329471" "5546562" "6263302" "6188975" "6332201" "6032268").pn.	("5911059" "6173419").pn.	("5663900" "6161199" "6347395" "6009270" "6185522" "6223144" "6173419" "6516428").pn.	("6202044" "5748875" "5978584" "6356862" "6718294").pn.	("5748875" "6202044" "5493723").pn.	(EDA) same ((internal with bus) with (PLA PLD FPGA MCM))	(EDA) with (PLA PLD FPGA MCM)	"09/954715"	"09/954715"	"09918600"	EDA same PCI same DMA	EDA same PCI	(hardware with software with (co-simulation cosimulation))
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S11	212	S14	\$15	S16	S17	818	S19	S20	221	275	83	524

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525	2	"6009256".pn.	É		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	e e	-PO	2005/06/03 12:26
S26	128	("3106698" "3473160" "4386403"	"3287702" "4020469" "4488354"	"3287703" "4306286" "4503386"	US-PGPUB; USPAT; USOCR	8	OFF.	2005/06/09 12:16
		"4541071"	"4577276" "4612618"	"4578761"				
		"4642487"	"4656580"	"4656592"				
		"4675832" "4697241"	"4682440" "4700187"	"4695999"				
		"4736338"	"4740919"	"4744084"				
		"4747102" "4768196"	"4752887"	"4758985" "4786904"				
		"4787061"	"4791602"	"4803636"				
		"4811214"	"4815003"	"4823276"				
		"482/427"	"4835705" "48623247	"4849904"				
		"4872125"	"4876466"	"4882690"	·			
		"4901259"	"4901260"	"4908772"				
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		"5253181"	"5258932"	"5259006"				
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		"5329470"		5352123"				
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		"5452231"	"5452239"	"5467462"				
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		"5563829"	"5612891"	"5644515"				
		"5649167"	"5649176"					
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		"6009531").PN.	- 146/996 - 1 - NA:	ו מרשבמתם				

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2005/06/03 15:44	2005/06/03 14:34	2005/06/03 14:21	2005/06/03 14:21	2005/06/03 14:30	2005/06/03 14:34	2005/06/03 14:44	2005/06/03 14:45	2005/06/03 14:50
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(US-5937179-\$ or US-5684721-\$ or US-5329471-\$ or US-6831268-\$ or US-6835682-\$ or US-6835682-\$ or US-6836887-\$ or US-6223144-\$ or US-6136897-\$ or US-628314-\$ or US-618875-\$ or US-628114-\$ or US-618247-\$ or US-6212489-\$ or US-605254-\$ or US-607593-\$ or US-6209120-\$) did. or (US-6212489-\$ or US-6209120-\$) did.	S27 and (eda and model and bus (reconfig\$ FPGA PLD) and memory)	S28 and DMA	S27 and DMA	S28 and (shared adj memory)	S27 and (eda and model and bus and (reconfig\$ FPGA PLD) and memory)	S27 not S28	(behavioral with (RTL gate)).ti.	(behavioral with synthesi≰G).b.
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227	828	S29	83	231	83	233	SS SS	88

Search History 11/29/2005 6:50:49 PM Page 5 C:\Documents and Settings\asaxena\PMy Documents\EAST\Workspaces\09954715.wsp

2005/06/03 14:50	2005/06/03 14:50	2005/06/03 14:50	2005/06/03 14:51	2005/06/03 14:54	2005/06/03 14:54	2005/06/03 14:55	2005/06/03 14:56	2005/06/03 14:56
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"7564951".pn.	"5764951".pn.	("4827427" "5111413" "5128871" "5237513" "5274793" "5437037" "5544066" "5572437").PN	("4353117" "4587625" "4635208" "4675832" "4697241" "4703435" "4789944" "4805113" "4813013" "4827427" "4811543" "4918614" "4920238" "4908772" "4918614" "4922432" "4965741" "4967365" "508484" "5111413" "5164908" "5164911" "5222030" "1940008" "1940020").PN	eda and ((shar‡3 adi3 memory) with (co-simulation cosimulation co-emulation coemulation coverification co-verification))	eda and (((shar\$3 common central host) adi3 memony) with (co-simulation cosimulation co-emulation co-emulation coverification))	eda and (((shar\$3 common central target) adj3 memory) with (co-simulation cosimulation co-emulation coverrification co-verification))	(hardware with design with tool) and (((shar43 common central target) adj3 memory) with (co-simulation cosimulation co-emulation coverification co-verification))	(design with tool) and (((shar\$3) common central target) adj3 memory) with (co-simulation cosimulation co-emulation co-emulation co-emulation co-emulation co-emulation)
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2005/06/03 14:57	2005/06/03 15:48	2005/06/06 11:36	2005/06/03 15:50	2005/06/03 16:40	2005/06/03 16:52	2005/06/03 16:53	2005/06/03 16:53	2005/06/03 16:53	2005/06/03 17:00	2005/06/03 17:00
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((shared common host joint target) with memory) and (hardware with software) and (co-verification coverification coverification)	"5838948".pn.	eda and control\$4 and DMA	("5815688" "5838948" "6134516"). PN.	(*4835736" "5058114" "5568437" "5572712" "5640542" "5717699" "5764079" "581771" "5870410" "596191" "6014334" "6016563" "6020758" "6157210" "6182247"). PN. OR ("6286114").URPN	(FPGA PLD (reconfigurable adj logic)) same memory same (hardware with software) same (cosimualton co-simulat\$6)	(FPGA PLD PLA) same memory same (hardware with software) same (cosimualtion co-simulat\$6)	(FPGA PLD PLA) same memory same (hardware with software)	(FPGA PLD PLA) with memory with (hardware with software)	("5889685" "6370675" "6083269" "5838947").pn.	("5889685" "6370675" "6083269" "5838947").pn.
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S57	137	quidktum.as.	USPAT; DERWENT	æ	OFF	2005/06/06 14:53
828	0	S57 and DMA	USPAT; DERWENT	క	9F0	2005/06/06 11:37
S29	0	S57 and (direct with memory with access)	USPAT; DERWENT	క	9F	2005/06/06 11:38
98	4	S57 and (shared with memory)	USPAT; DERWENT	ĕ	PF0	2005/06/06 11:39
S61	25	S57 and (memory with access)	USPAT; DERWENT	ĕ	F O	2005/06/06 11:39
S62	e e	S60 and S61	USPAT; DERWENT	ĕ	뜌	2005/06/06 11:39
8	23	(US-5937179-\$ or US-5684721-\$ or US-5838471-\$ or US-6691268-\$ or US-6838877-\$ or US-52546562-\$ or US-635682-\$ or US-635682-\$ or US-6223144-\$ or US-6326821-\$ or US-623900-\$ or US-620304-\$ or US-6212489-\$ or US-602224-\$ or US-607231-\$ or US-629330-\$ or US-629330-\$ or US-629330-\$ or US-5293161-\$ or US-5815688-\$ or US-5283161-\$ or US-5661662-\$).did.	USPAT; DERWENT	ĕ	ь	2005/06/07 14:06
8 8	3	S63 and (bus) and (shared with memory)	USPAT; DERWENT	క	OFF	2005/06/06 14:24
S65	92	(bus) and (shared with memory) and (eda (electronic with design with automation))	USPAT; DERWENT	ğ	OFF	2005/06/06 14:24
98	80	S63 and (state with machine)	USPAT; DERWENT	క	FP.	2005/06/06 14:44
292	72	quicktum.as.	USPAT	క	HO.	2005/06/06 14:53
88	145	((internal with bus) with (reconfigurable PLA PLD FPGA MCM))	USPAT	æ	OFF	2005/06/06 15:19
698	15	S68 and ((shar\$4 with memory) and DMA)	USPAT	క	-FFO	2005/06/06 15:20
S70	3	("5197130" "5784630" "6167502"). PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/06 15:23

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(DMA with PCI) and (emulat\$4 simulat\$4 model\$4)

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US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB

(DMA with PCI with (reconfig\$6 FPGA PLD PLA MCM)) and (emulat\$4 simulat\$4 modet\$4)

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S74

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(DMA with (reconfig\$6 FPGA PLD PLA MCM)) and (emulat\$4 simulat\$4 model\$4)

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S75

US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB

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S75 not S74

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S76

US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB

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USPAT

"6321366".pn. US20020152060\$A

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S77 S81

US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB

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("5838948" "5815688").pn.

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US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB

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USPAT

"5450551".pm

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16:51	17 14:48	15:06	70:51 20	72:57	72:27	07 15:28	07 15:33	07 17:45
2005/06/08 16:51	2005/06/07 14:48	2005/06/07 15:06	2005/06/07 15:07	2005/06/07 15:27	2005/06/07 15:27	2005/06/07 15:28	2005/06/07 15:33	2005/06/07 17:45
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USPAT; DERWENT	USPAT; DERWENT	USPAT; DERWENT	USPAT; DERWENT	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB				
(US-5937179+ or US-5684721+5 or US-5329471-4 or US-6691268+5 or US-6328487-4 or US-6591268-5 or US-632865-2 or US-6388367-4 or US-6286114+5 or US-6188975-5 or US-6286114+5 or US-620204+5 or US-6286114+5 or US-602524+5 or US-602835-5 or US-62298320-4 or US-6108494+5 or US-6298320-4 or US-6108494+5 or US-5298320-4 or US-5815688-4 or US-583848-4 or US-583848-4 or US-5808161-4 or US-5981562-4 or US-590432-4).did.	S84 and (plural\$5 and reconfig\$8)	FPGA with interconnect\$4	S86 and (hyper-cube hypercube)	series adj FPGA	(series adj interconnect\$4) with FPGA	(series with interconnect\$4) with FPGA	daisy with chain with FPGA	703/20.acts.
78	6	1105	6	136	0	21	22	165
*	88	88	88	88	88	88	165	292

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2005/06/07 17:47	2005/06/07 17:49	2005/06/07 17:52	2005/06/07 17:51	2005/06/07 18:13	2005/06/07 18:13	2005/06/08 11:01	2005/06/08 11:01
PFO	-0FF	FF0		PF 0	OFF	OFF	0FF
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US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
719/312,313,318,319,310.cds.	S93 and unix and (verilog vhdl (hardware adj descript\$6 adj language))	S93 and (verilog vhdl (hardware adj descript\$6 adj language)) and (simulat\$4 emulat\$4)	S93 and (verliog vhdl (hardware adj descript\$6 adj language))	S93 and (simulat\$4 emulat\$4)	"5109353".pn.	software with hardware with pointer with mapping	hardware with software with mapp\$4
1926	4	10	15	330	2	10	1129
293	2 88	295	965	265	865	665	000

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2005/06/08 11:01	2005/06/08 11:04	2005/06/08 11:06	2005/06/08 11:07	2005/06/08 11:08	2005/06/08 16:50	2005/06/08 16:51	2005/06/08 16:54
FF0	PF-0	OFF	OFF	OFF	THO OFF	E O	OFF
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US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	USPAT; DERWENT	USPAT; DERWENT
S100 and pointer	S101 not S99	(control with logic) and ((data-in with pointer with logic) or (data-in with latch with logic))	(control with logic) and ((data-in with pointer with logic))	(pointer with based) same (hardware with software with mapping)	(pointer) same (hardware with software with mapping)	(US-5937179+\$ or US-5684721-4; or US-5329471-4; or US-6691268-4; or US-63268-2; or US-632314-4; or US-632868-2; or US-632868-3; or US-63286114-5; or US-6286114-5; or US-6286114-5; or US-6286114-5; or US-6286114-5; or US-605224-4; or US-607533-5; or US-605224-5; or US-6298120-5; or US-6298120-5; or US-5815688-5; or US-588168-4; or US-588168-5; or US-581568-5; or US-581568-5; or US-581568-5; or US-598161-5; or US-581568-5; or US-598161-5; or US-599512-4; or US-599512-4; or US-5970240-5; or US-6094532-4; or US-6094532-4; or US-6094532-4; or US-60945317-4;).	S107 and pointer
349	339	4	Φ.	4	17	8	9
S10 1	S10 2	33	\$10 4	5 5	S10 6	210	S10 8

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S10 9	215	hardware with software with pointer	USPAT; DERWENT	ĕ	94 1	2005/06/08 16:54
	18	S109 with (simulat\$4 emulat\$4 model\$6)	USPAT; DERWENT	æ	OFF.	2005/06/08 19:47
	٦	"5867541".pn.	USPAT	æ	HO.	2005/06/08 19:53
	4	("5649176" "5659716" "5761097" "6009531").pn.	USPAT	æ	붠	2005/06/08 19:54
	1072	delay with insensitive	US-PGPUB; USPAT; USOCR	ĕ	OFF	2005/06/09 12:18
	476	delay adj3 insensitive	US-PGPUB; USPAT; USOCR	ĕ	OFF	2005/06/09 12:18
-	S,	(delay adj3 insensitive) with asynchronous	US-PGPUB; USPAT; USOCR	æ	OFF	2005/06/09 12:23
	4	(timing with insensitive) same (trigger latch)	US-PGPUB; USPAT; USOCR	క	OFF	2005/06/09 12:25
	201	(timing with insensitive)	US-PGPUB; USPAT; USOCR	Ř	OFF	2005/06/09 12:25
	17	S118 and ((glitch adj2 free) or glitchless)	US-PGPUB; USPAT; USOCR	క	OFF	2005/06/09 12:28
S12 0	210	(glitch near free) and (trigger or latch or flip\$flop)	US-PGPUB; USPAT; USOCR	క	F.O	2005/06/09 12:28
S12 1	521	(glitch with free) and (trigger or latch or flip\$flop)	US-PGPUB; USPAT; USOCR	క	9FE	2005/06/09 12:30
S12 2	218	(glitch with free) same (trigger or latch or flip\$flop)	US-PGPUB; USPAT; USOCR	క	F.	2005/06/09 12:46
S12 3	01	((gated or derived) near clock) same ((glitch adj less) or glitchfree or (glitch adj free) or glitchless)	US-PGPUB; USPAT; USOCR	క	PF.	2005/06/09 12:49
····	σ	FPGA same (external adj I/O) same simulat\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	8	Ŧ.	2005/06/09 15:32

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15:32	15:33	15:33	15:33	15:33	15:34	15:34	15:38
2005/06/09 15:32	2005/06/09 15:33	2005/06/09 15:33	2005/06/09 15:33	2005/06/09 15:33	2005/06/09 15:34	2005/06/09 15:34	2005/06/09 15:38
FP	PF0	PF0	OFF.	FF C	PF.	OFF	PF-
8	Ř	R	8	ĸ	8	8	g
US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB				
reconfig\$6 same (external adj I/O) same simulat\$4	(PLA PLD FPCA MCM) same (external adj I/O) same simulat\$4	(PLA PLD FPGA MCM) same (external adj device) same simulat\$4	FPGA same (external adj device) same simulat\$4	FPGA same (external adj device) same (simulat\$4 emaulat\$4 model\$4)	FPGA same (external adj I/O) same (emaulat\$4 model\$4)	FPGA same (external adj I/O)	S132 not S126
6	თ	0	0	0	6	8	39
S12 6	512	8 8	9	S13 0	1 1	2 2	33

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(external with I/O) with (simulat\$4 emaulat\$4 model\$4)

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S13

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S135 not S132

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US-PGPUB; USPAT; USOGR; EPO; JPO; DERWENT; IBM_TDB USPAT; USOGR; EPO; JPO; DERWENT; IBM_TDB USPAT; U

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read write multiple FPGA

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(multiple with FPGA) same (access interface I/O read write)

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S13

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(multiple with FPGA) same (access interface I/O read write)

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S14 0

US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB

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US-PGPUB; USPAT; USOCR

("5497498" | "5535342" | "5539330" | "588749" | "5603043" | "5628028" | "5784636" | "5788669" | "5794062" | "5991822" | "599990" | "6185484" | "6230307").PN

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S14

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bus with selector with FPGA

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S14 2

US-PGPUB; USPAT; USOCR

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bus with selector and FPGA

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S14 3

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US-PGPUB; USPAT; USOCR US-PGPUB; USPAT; USOCR

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S14 5

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S145 and FPGA

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2005/06/09 17:57	2005/06/09 17:58	2005/06/10 09:36	2005/11/29 16:13
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US-PGPUB; USPAT; USOCR	US-PGPUB; USPAT; USOCR	USPAT; DERWENT	US-PGPUB
703/26,19.cds.	S147 and (FPGA reconfig\$6 PLA PLD MCM)	(US-6169422.\$ or US-5387825.\$ or US-5808468.\$ or US-5383330.\$ or US-5109333.\$ or US-5380330.\$ or US-536030.\$ or US-536351.\$ or US-536350.\$ or US-536350.\$ or US-536350.\$ or US-5684721.\$ or US-590383.\$ or US-590383.\$ or US-591059.\$ or US-5903833.\$ or US-591059.\$ or US-5903833.\$ or US-5903816.\$ or US-5903833.\$ or US-6094332.\$ or US-6094332.\$ or US-6094332.\$ or US-6039323.\$ or US-623644.\$ or US-623830.\$ or US-623861.\$ or US-623830.\$ or US-632862.\$ or US-633862.\$ or US-633862.\$ or US-633862.\$ or US-633863.\$ or US-633863.\$ or US-633863.\$ or US-633863.\$ or US-63384.\$ or US-63384.\$ or US-633843.\$ or US-6338	"09/954715"
537	65	6	0
S14 7	S14 8	0 0	S15 1

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